

<b>Notice of References Cited</b>	Application/Control No. 10/695,004	Applicant(s)/Patent Under Reexamination ADAMS ET AL.	
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#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,526,462	02-2003	Elabd, Hammam	710/242
	C	US-5,935,232	08-1999	Lambrecht et al.	710/315
	D	US-6,874,039	03-2005	Ganapathy et al.	710/28
	E	US-6,769,046	07-2004	Adams et al.	710/316
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	V	OpenCores SOC Bus Review, Usselmann, 1/2001
	W	System-on-a-Chip Bus Architecture for Embedded Applications, ARM Limited.
	X	SiliconBackplane III MicroNetwork IP, Sonics, 2002.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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